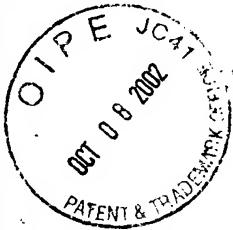


Serial No. 10/007,833



IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

**PATENT APPLICATION**

Applicant: **AVERY**

File: **SAR 14179**

Serial No.: **10/007,833**

Examiner: **Tan N. Tran**

Filed: **November 5, 2001**

Group Art Unit: **2826**

Title: **SILICON CONTROLLED RECTIFIER ELECTROSTATIC  
DISCHARGE PROTECTION DEVICE WITH EXTERNAL  
ON-CHIP TRIGGERING AND COMPACT INTERNAL  
DIMENSIONS FOR FAST TRIGGERING**

ASSISTANT COMMISSIONER FOR PATENTS  
Box Non-Fee Amendment  
Washington, D.C. 20231

SIR:

AMENDMENT UNDER 37 C.F.R. 1.111

In response to the non-final office action dated July 3, 2002 (Paper No. 3),  
please amend the above-identified patent application as follows:

IN THE DRAWINGS

Please amend Figs. 1A, 1B, and 2A as indicated in red ink in the drawings.

IN THE SPECIFICATION

Please replace the following paragraphs as rewritten below.

Paragraph number 0006:

The triggering device 105 is illustratively a grounded gate NMOS (GGNMOS) transistor, which has its source 127 and gate 126 coupled to ground. Additionally, the drain 129 and source 127 of the GGNMOS transistor 105 are respectively coupled to the collector 110 and the emitter 112 of the NPN transistor T1 131. Furthermore, the gate 126 and source 127 of the GGNMOS transistor are also connected to the grounded third node 124 (i.e., cathode of the SCR).

TECHNICAL DRAWINGS  
JULY 12, 2002  
12300

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